

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Seung-Jae LEE and Jin-Gi HONG

Serial No.: 10/626,096 Examiner: Vu, David

Filed: July 23, 2003 Group Art Unit: 2818

Confirmation No.: 9000

For: GATE ELECTRODE OF A SEMICONDUCTOR DEVICE AND
METHOD OF FORMING THE SAME

May 25, 2006

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

In the Examiner's Statement of Reasons for Allowance, the Examiner recited portions of the allowed independent claims and stated that the prior art failed to teach the recited portions. The applications note that it is a well known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

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Respectfully submitted,
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